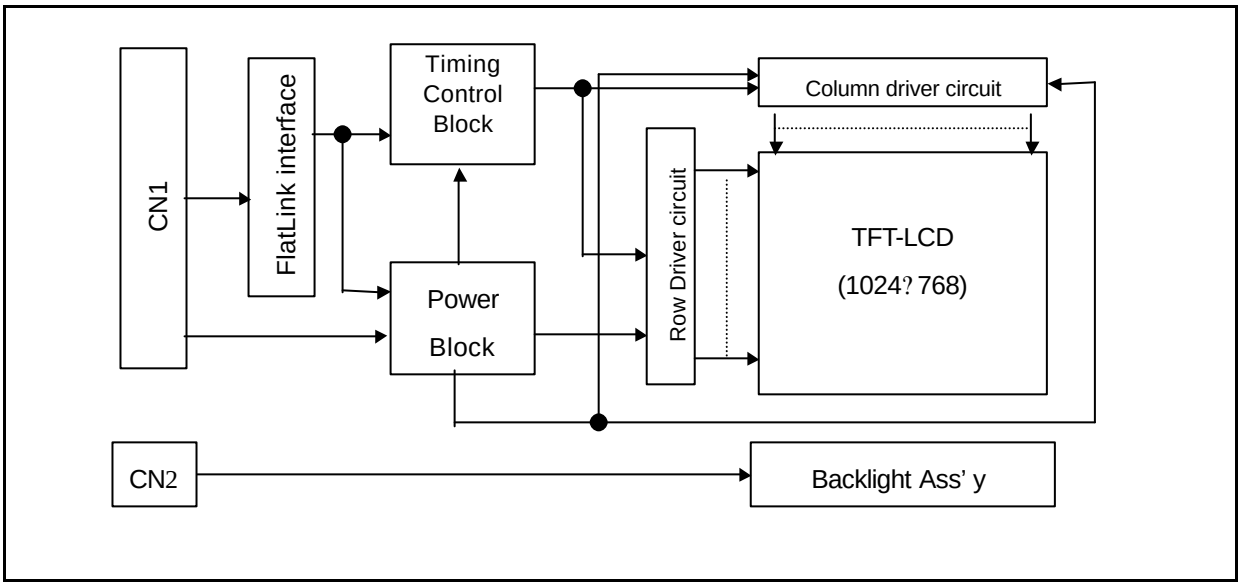
1. General Description

The LP133X7 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) back light system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has a 13.3 inch diagonally measured active display area with XGA resolution(768 vertical by 1024 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP133X7 has been designed to apply the interface method that enables low power, high speed low EMI. Flat Link must be used as a LVDS(Low Voltage Differential Signaling) chip.

The LP133X7 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP133X7 characteristics provide an excellent flat panel display for office automation products such as Notebook PC.



General Features

Active screen size	13.3 inches(33.78cm) diagonal
Outline dimensions	284(H) × 214.5(V) × 5.6(D) mm (typ)
Pixel pitch	0.264 mm × 0.264 mm
Pixel format	1024 horiz. By 768 vert. pixels RGB stripe arrangement
Color depth	6bit, 262,144 colors
Luminance, White	140 cd/m^2 (typ)
Power Consumption	Total 4.6Watt(typ)
Weight	480g (typ)
Display operating mode	transmissive mode, normally white
Surface treatments	hard coating(3H), anti-glare treatment of the front polarizer

2. Electrical Specifications

2-1. Electrical Characteristics

The LP133X7 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 1 ELECTRICAL CHARACTERISTICS:

Parameter	Symbol	Values			Units	Notes
		Min.	Typ.	Max.		
MODULE:						
Power Supply Input Voltage	V_{CC}	3.0	3.3	3.6	Vdc	
Power Supply Input Current	I_{CC}	0.205	0.235	0.265	A	1
Differential Impedance	Zm	90	100	110	ohm	2
Power Consumption	P_c	0.68	0.77	0.87	Watts	1
Rush current	I_{RUSH}	-	1.5	1.8	A	3
LAMP						
Operating Voltage	V_{BL}	635	650	810	V_{RMS}	4
Operating Current	I_{BL}	3.0	6.0	6.5	mA	
Established Starting Voltage						5
at 25℃		-	-	1100	V_{RMS}	
at 0℃		-	-	1450	V_{RMS}	
Discharge Stabilization Time	Ts			3	min	7
Operating Frequency	f_{BL}	40	55	70	kHz	6
Power Consumption	P_{BL}	2.43	3.9	4.13	Watts	8
Life Time		10,000	15,000	-	Hrs	9

Notes: **The design of the inverter must have specifications for the lamp in LCD Assembly.**

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC Inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.

When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter(no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.

1. The specified current and power consumption are under the Vcc = 3.3V , 25℃, fv = 60Hz condition whereas 64 gray pattern is displayed.
2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
3. The duration of rush current is about 20ms.
4. The variance of the voltage is ±10%.
5. The voltage above V_s should be applied to the lamps for more than 1second for start-up. Otherwise, the lamps may not be turned on.
6. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Unsymmetrical ratio is less than 10%) Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave.
   Lamp frequency may produce interference with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away as possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
7. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%.
   T_s is the time required for the brightness of the center of the lamp to be not less than 95%.
8. The lamp power consumption shown above does not include loss of external inverter.
9. The life time is determined as the time at which brightness of lamp is 50% compared to that of initial

value at the typical lamp current on condition of continuous operating at 25 ±2℃.

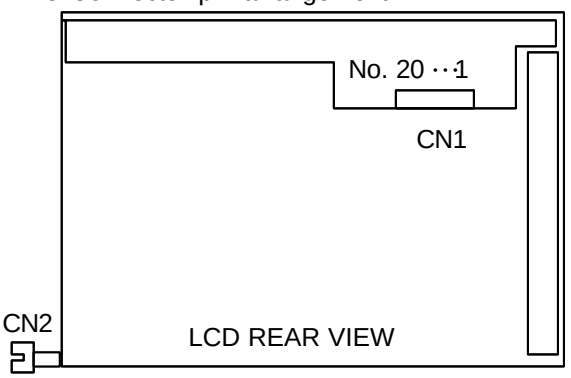
2-2. Interface Connections

Interface chip must be used FlatLink ,part No. THC63LVDM63A(Transmitter) , KZ4E038C12CFP(Receiver) made by THine Microsystems,Inc.

This LCD employs two interface connections, a 20 pin connector is used for the module electronics and the other connector is used for the integral backlight system.

The electronics interface connector is a model DF19KR-20P-1Hmanufactured by HIROSE. The pin configuration for the connector is shown in the table below.

Table 2 MODULE CONNECTOR PIN CONFIGURATION (LVDS) [CN1]

Pin	Symbol	Description	Notes
1	VCC	Power (3.3V)	1. Interface chips 1.1 LCD : KZ4E038C12CFP (THC63LVDF64A Core) 1.2 System : THC63LVDM63A 48TSSOP * Pin to Pin compatible with TI LVDS 2. Connector 2.1 LCD : DF19KR-20P-1H(HIROSE) or GT100-20-LS-SMT-R(LG CABLE) → Pin to Pin compatible with HIROSE 2.2 Mating -. Wire type : DF19G-20S-1C(HIROSE) -. FPC type : DF19-20S-1F(HIROSE) 2.3 Connector pin arrangement 
2	VCC	Power (3.3V)	
3	GND	Ground	
4	GND	Ground	
5	A0M	Difference Signal	
6	A0P	Difference Signal	
7	GND	Ground	
8	A1M	Difference Signal	
9	A1P	Difference Signal	
10	GND	Ground	
11	A2M	Difference Signal	
12	A2P	Difference Signal	
13	GND	Ground	
14	CLKM	Difference Signal	
15	CLKP	Difference Signal	
16	GND	Ground	
17	NC	No Connection	
18	NC	No Connection	
19	GND	Ground	
20	GND	Ground	

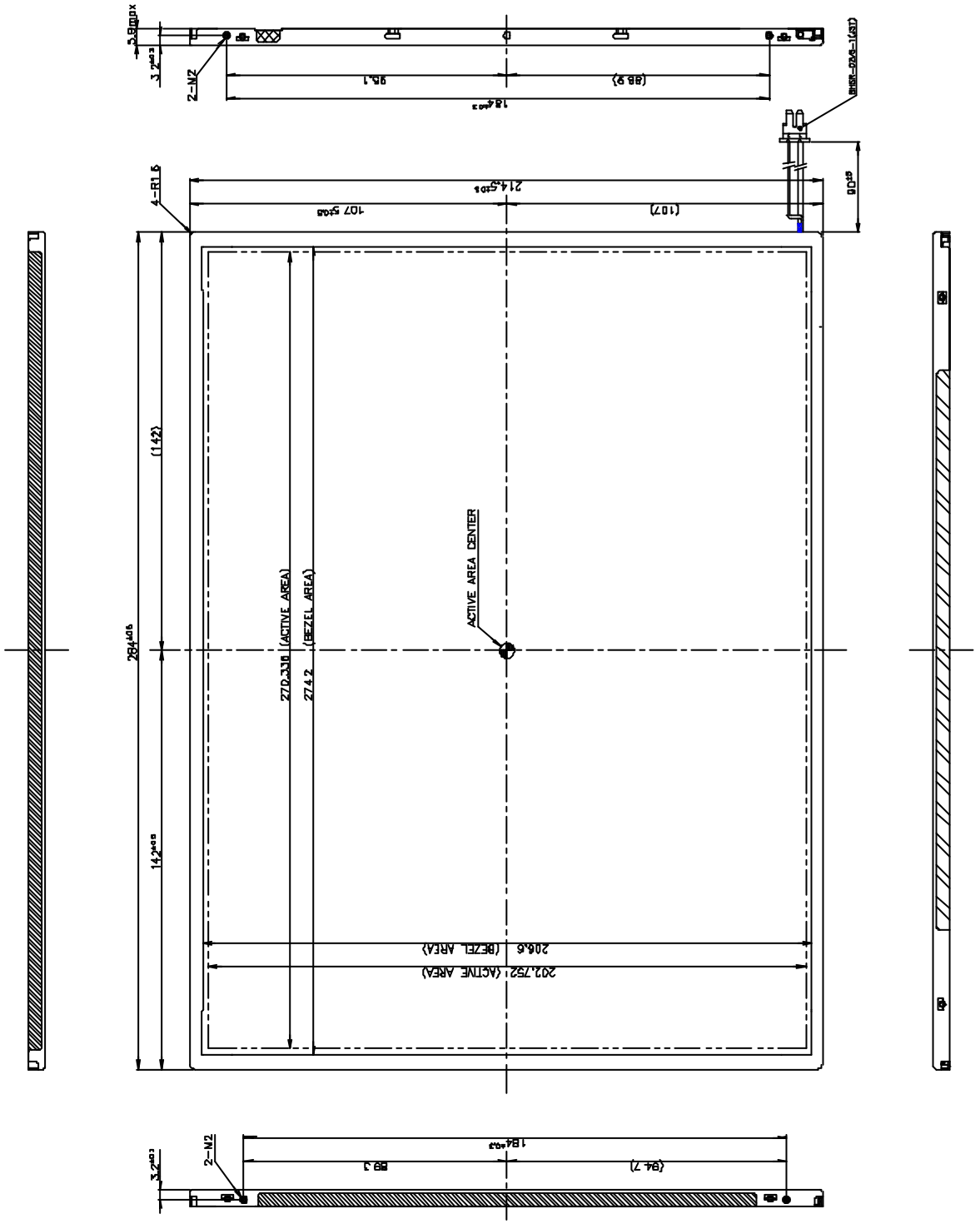
The backlight interface connector is a model BHSR-02VS-1, manufactured by JST. The mating connector part number is SM02B-BHSS-1 or equivalent. The pin configuration for the connector is shown in the table below.

Table 3 BACKLIGHT CONNECTOR PIN CONFIGURATION [CN2]

Pin	Symbol	Description	Notes
1	HV	High voltage input	1
2	LV	Low voltage input	2

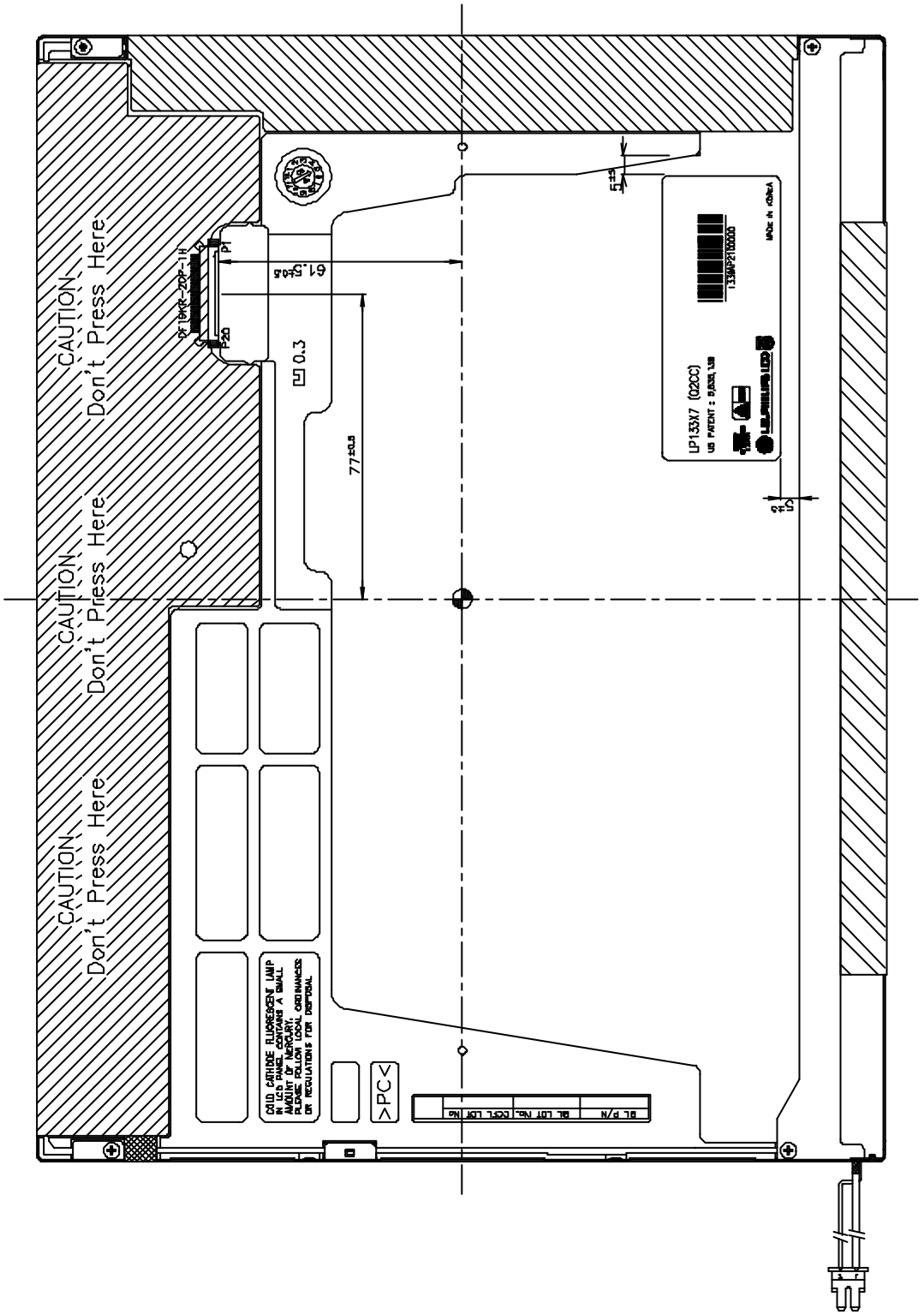
Notes: 1. The high voltage input terminal is colored white.
2. The low voltage input terminal is colored black.

< FRONT VIEW >



Notes 1. Unspecified dimensional tolerance are ±0.5mm

< REAR VIEW >


CAUTION
Don't Press Here
CAUTION
Don't Press Here
CAUTION
Don't Press Here
61.5±0.5
77±0.5
0.3
LP133X7 (D2CC)
COLD CATHODE FLUORESCENT LAMP IN LCD PANEL CONTAINS A SMALL AMOUNT OF MERCURY; PLEASE FOLLOW LOCAL ORDINANCES OR REGULATIONS FOR DISPOSAL
>PC<

3.PRECAUTIONS

The LCD Products listed on this documents are not suitable for use of Military,Industry,Medical etc. system.
If customers intend to use these LCD products for above application, Please contact ours sales people in advance.