

Display type: IBM ITSV50E
 Inverter no.: not available
 Cable no.: 72570100 (If PanelLink solution then 72592100)
 System: 686LCD with FPUM-1
 Display driver: 800 x 600, TFT, IBM, ITSV50 and 5V
 Status: Verified

Display	40-pole conn.	JPLCD	50-pole conn.	Remarks
Function	Pin	Pin	Function	
GND	1	*	GND	
BLUE5	2	25	P7	
BLUE4	3	24	P6	
BLUE3	4	22	P5	
GND	5	*	GND	
BLUE2	6	21	P4	
BLUE1	7	19	P3	
BLUE0	8	18	P2	
GND	9	*	GND	
GREEN5	10	37	P15	
GREEN4	11	36	P14	
GREEN3	12	34	P13	
GND	13	*	GND	
GREEN2	14	33	P12	
GREEN1	15	31	P11	
GREEN0	16	30	P10	
GND	17	*	GND	
RED5	18	49	P23	
RED4	19	48	P22	
RED3	20	47	P21	
GND	21	*	GND	
RED2	22	46	P20	
RED1	23	44	P19	
RED0	24	43	P18	
GND	25	*	GND	
Hsync	26	10	LP	
Vsync	27	11	FLM	
DSPTMG	28	7	M	
VDD	29	1	VCC	
VDD	30	2	VCC	
GND	31	*	GND	
GND	32	*	GND	
Reserved	33	-	-	No connection
Reserved	34	-	-	No connection
GND	35	*	GND	
-DTCLK	36	13	SHFCLK	Dot clock
GND	37	*	GND	
Reserved	38	-	-	No connection
Reserved	39	-	-	No connection
GND	40	*	GND	

* Pin 6, 9, 12, 14, 17, 20, 23, 26, 29, 32, 35, 38, 45 & 50

Inverter 16-pol. conn.		INV60-01 8-pole		Connect to:
Function	Pin	Function	Pin	
VBL	1	IN	1 & 2	JPLCD pin 40
VBL	2	IN	1 & 2	JPLCD pin 40
VBL	3	IN	1 & 2	JPLCD pin 40
VBL	4	IN	1 & 2	JPLCD pin 40
VBL_RTN	5	RTN	3	Connect to GND
VBL_RTN	6	RTN	3	Connect to GND
VBL_RTN	7	RTN	3	Connect to GND
VBL_RTN	8	RTN	3	Connect to GND
BL_ON	9	BLON	7	JPLCD pin 8
Reserved	10	-	-	No connection
Brightness Control	11	BR	6	JPLCD pin 1
Reserved	12	-	-	No connection
GND	13	GND	4	JPLCD pin 6
VDIM_IN	14	CHOKO	8	*
VDD	15	VDD	5	JPLCD pin 1
VDIM_OUT	16	-	-	**

* If external potentiometer (2K2 Ohm) shall be used then connect to center pin of potentiometer, otherwise this signal shall be connected to VDIM_OUT. The two other terminal of the potentiometer shall be connected to GND and VDD respectively.

** If internal potentiometer shall be used, instead of external potentiometer, then connect this signal to VDIM_IN, otherwise do not connect this signal.